

FIG. 1

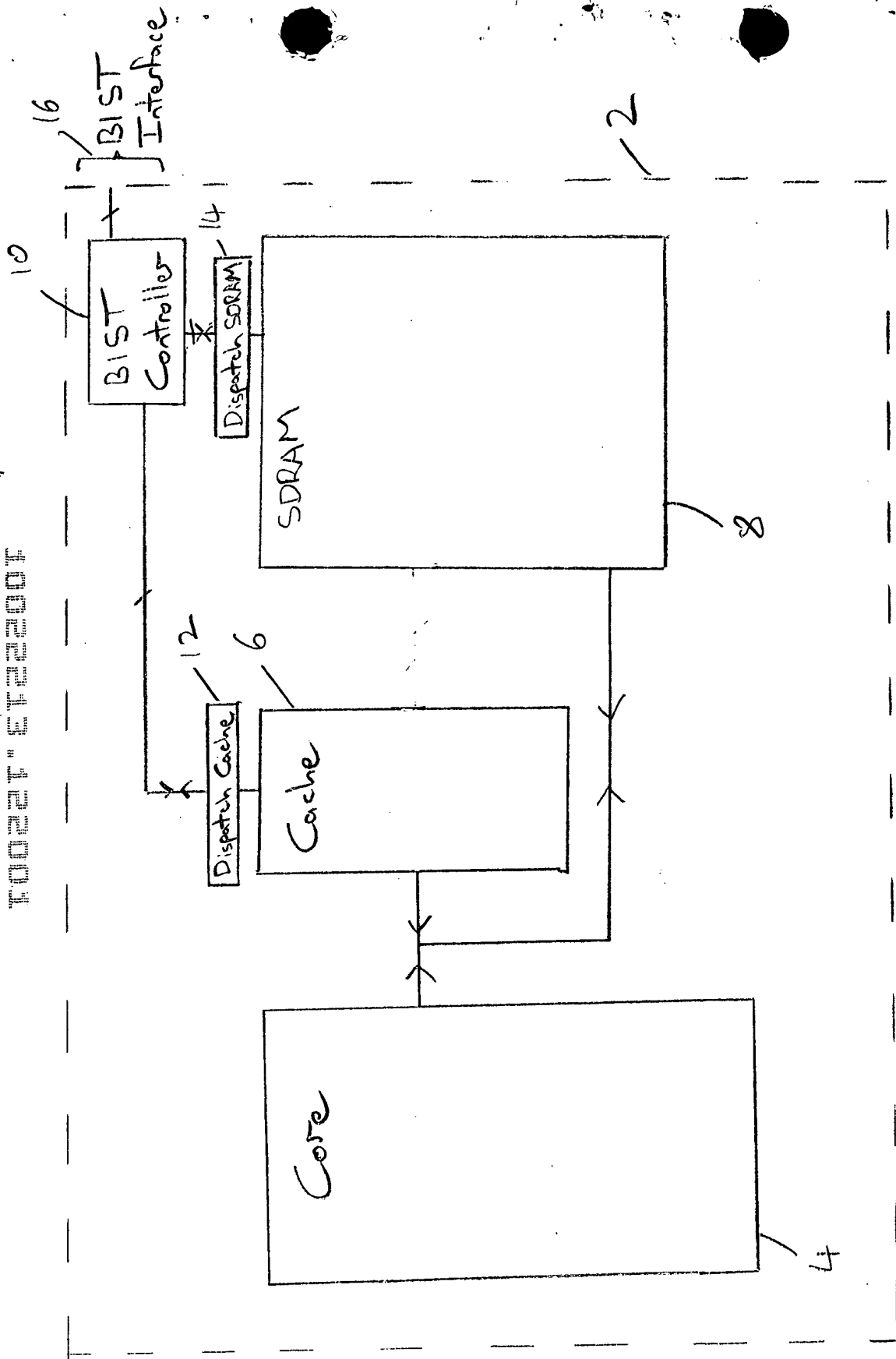


Fig. 1

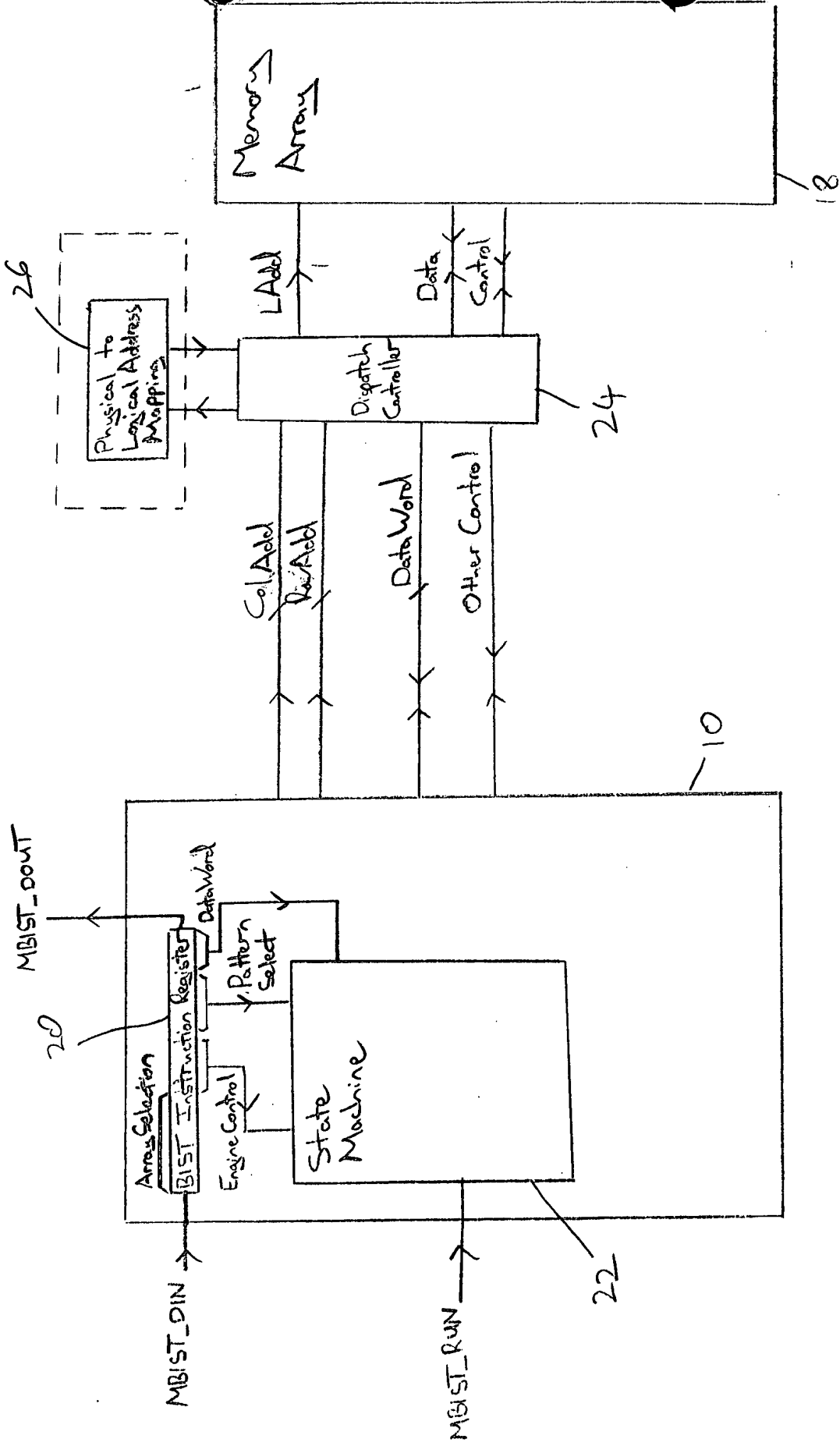


Fig. 2

10022213.122001

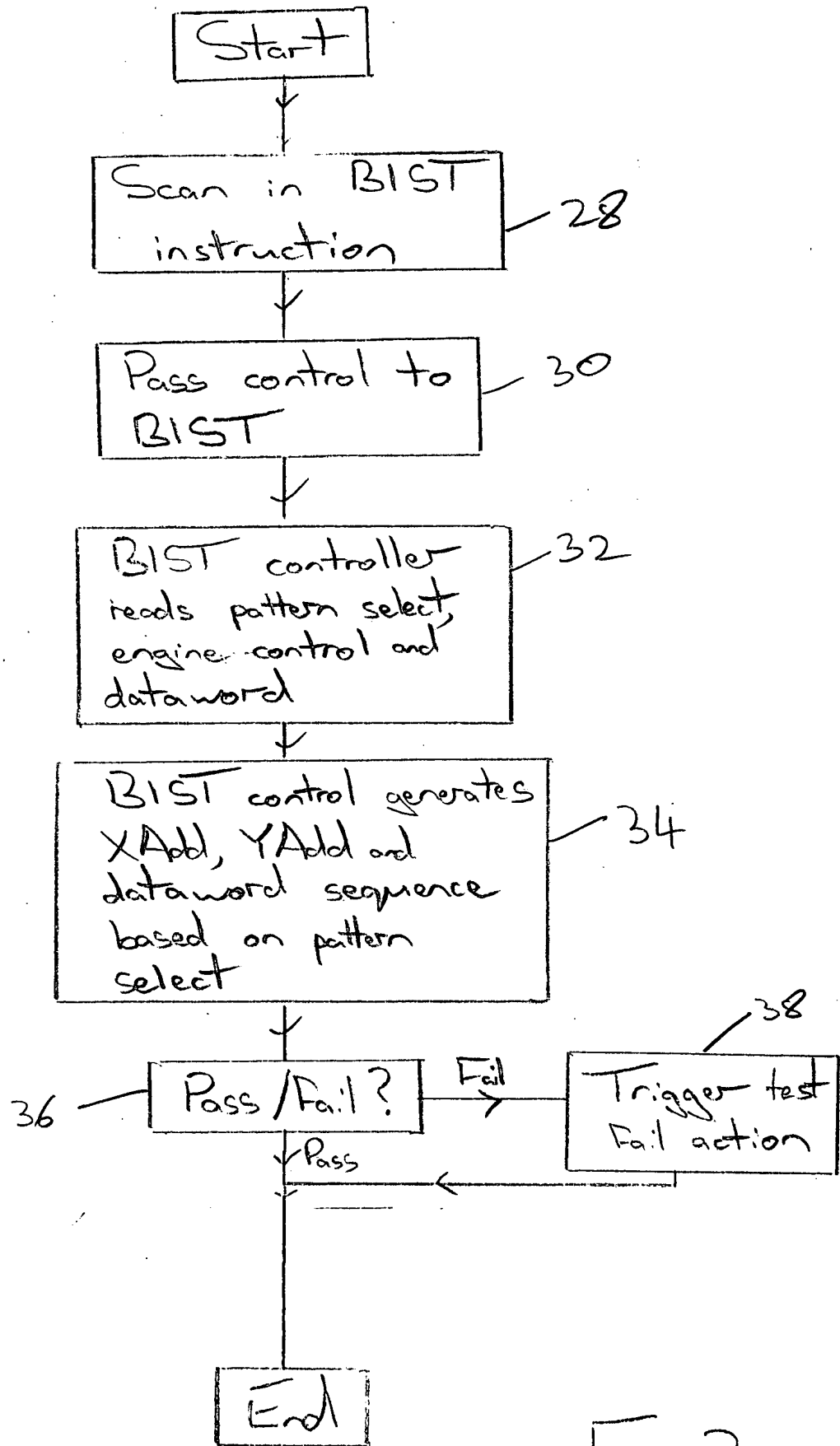


Fig. 3

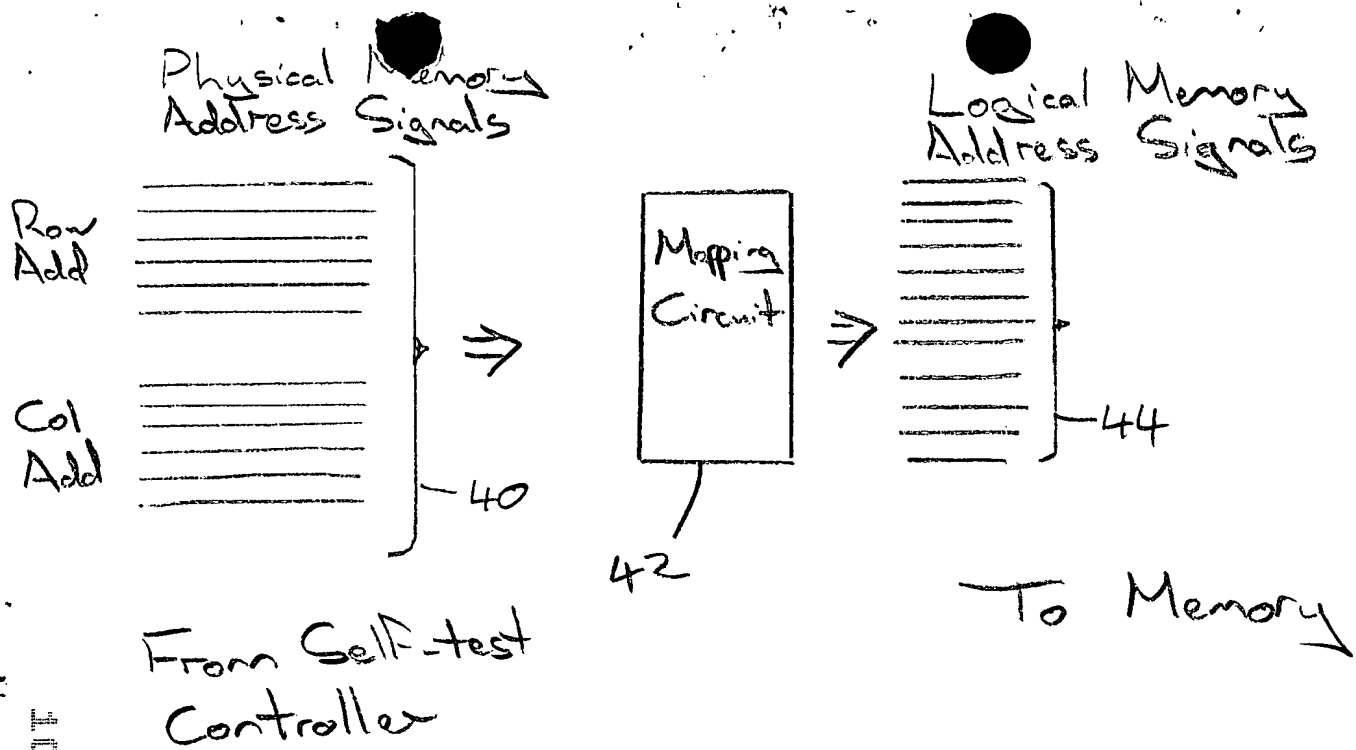
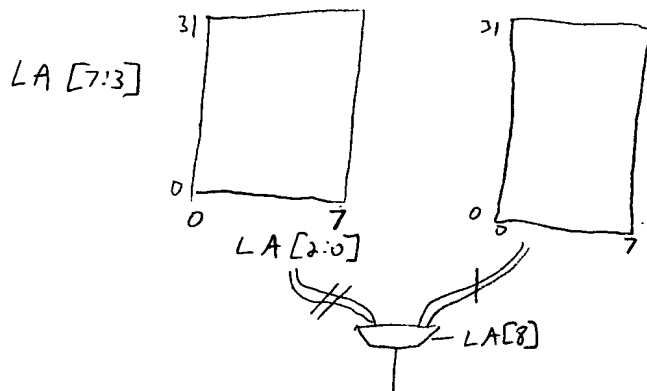


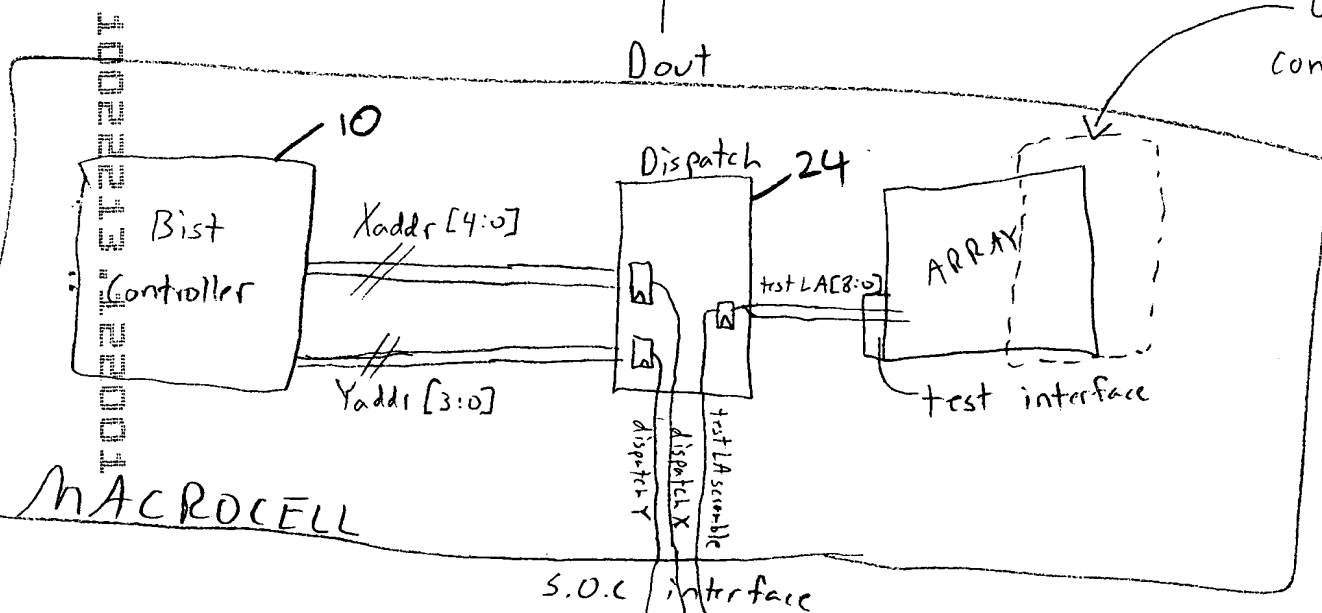
Fig. 4

10022213.122001

LA [8:0] when implemented has 32 rows, 8cols, 1 block select



user/integrator:
compiled RAM



S.O.C interface

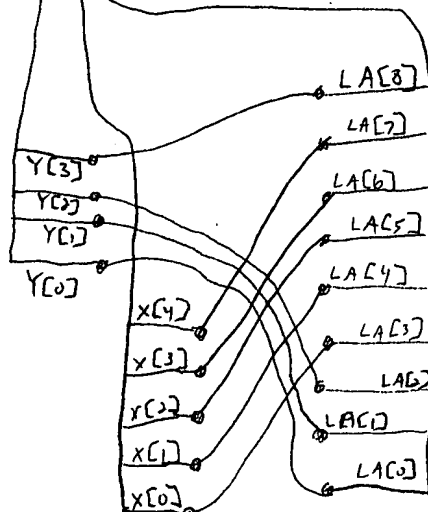


Fig. 5